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8-19-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: Alon et al.

Confirmation No.: 7058

Serial No.: 10/091,934

Group Art Unit: 2825

Filed: March 06, 2002

Examiner: Naum B Levin

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED
CIRCUIT DESIGN

August 4, 2003

AmendmentHonorable Commissioner for Patents
P.O. 1450
Alexandria, Virginia 22313-1450RECEIVED
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TC 2800 MAIL ROOM

Sir:

This amendment is filed in response to the Office Action dated 4 April 2003 issued by the USPTO in connection with the above-identified patent application. A response to the 4 April 2003 Office Action was due 4 July 2003. Applicants are concurrently filing a Petition for a one-month extension of time, including the required fee. Therefore a response is due 4 August 2003. Accordingly this Amendment is being timely filed.

The Amendments contained herein are submitted in accordance with the Revised Amendment Format under the waiver of 37 C.F.R. §1.121 as issued by the Deputy Commission for Patent Examination Policy on 1/31/03.

Applicant submits the following amendments and remarks:

08/07/2003 AWONDAF1 00000074 090468 10091934

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